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## Complete if Known

Application Number	10/644,225
Filing Date	August 19, 2003
First Named Inventor	Igor Keller
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket No.	CA7017522001

INFORMATION DISCLOSURE  
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## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YY	Name of Patentee or Application of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	* <sup>2</sup>
APL		L. McMurchie and C. Sechen. WTA - Waveform-Based Timing Analysis for Deep Submicron Circuits. In Proc. IEEE/ACM International Conference on Computer-Aided Design, 2002	
APL		N.H.E. Weste and K. Eshraghian, "Principles of CMOS VLSI Design," 2 <sup>nd</sup> edition, pages 442-446, 469-471, 645-653, Addison-Wesley Publishing Company 1993	
APL		J. Qian, S. Pullela, and L.T. Pillage, "Modeling the 'Effective Capacitance' for the RC Interconnect of CMOS Gates," IEEE Transactions on CAD, vol. 13, No. 12, pp. 1526-1535, 1994	

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